

1C602 U. S. PTO
09/768987
01/23/01

257	77	Subclass
	Class	ISSUE CLASSIFICATION

PATENT NUMBER
6441496

6441496

U.S. UTILITY Patent Application

 SCANNED <u>HKM</u> (P) Q.A. <u>HT</u>	O.I.P.E.	PATENT DATE
	AUG 27 2002	

CLASS 251	SUBCLASS	ART UNIT 2822	EXAMINER
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APPLICATION NO. 09/768987	CONT/PRIOR	CLASS 257	SUBCLASS 777	ART UNIT 2822	EXAMINER Porter
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APPlicants
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Structure of stacked integrated circuits and method for manufacturing the same

PTO-2040
12/89

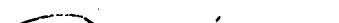
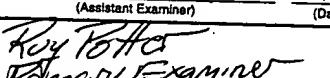
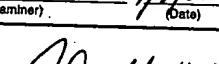
ISSUING CLASSIFICATION

Continued on Issue Slip Inside File Jacket

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Formal Drawings (/ shfs) set /

1/23/01

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	1	4	2	6	1
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____	 (Assistant Examiner) _____  Primary Examiner Technology Center 2800  Ray _____ (Primary Examiner) _____ (Date) 4/7/02			NOTICE OF ALLOWANCE MAILED 4/8/02	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	 (Legal Instruments Examiner) _____ (Date) 4/10/02			Amount Due \$940.00	Date Paid 7-15-02
ISSUE BATCH NUMBER					

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Formal drawings (SNIS) set)

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